



IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: HELLER ET AL.	: Group Art Unit: 2187
Serial No.: 10/603,252	: Examiner: RUTZ, JARED IAN
Filed: June 25, 2003	:
Confirmation No.: 3685	:
Title: Multiprocessor computer system having multiple coherency regions and software process migration between coherency regions without cache purges	: International Business Machines Corporation : 2455 South Road, Mail Station P386 : Poughkeepsie, NY 12601

**AMENDMENT UNDER 37 C.F.R. 1.312 AFTER ALLOWANCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Notice of Allowance dated August 6, 2008, please amend this application as indicated below:

**Amendments to the Specification begin on page 2.**

**Remarks begin on page 3.**

OK TO ENTER: /J.R./

11/13/2008